

Notice of References Cited

Application/Control No.

09/902,497

Applicant(s)/Patent Under
Reexamination
DUTARTRE ET AL.

Examiner

Matthew A. Anderson

Art Unit

1765

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*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
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	U	Wolf et al., Silicon Processing for the VLSI Era Volume 1: Process Technology, Lattice Press, Sunset Beach, CA, USA, pp. 124-159, 242-279, 1986.
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*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.

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